

Aditya Umesh

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SUMMARY

UI/UX Designer and Electronics & Telecommunication Engineering graduate with a strong interest in designing intuitive mobile and web interfaces. Skilled in Figma, wireframing, interactive prototyping, design systems, and responsive design. Passionate about creating user-centered experiences and eager to learn, collaborate, and grow in a professional design environment.

EDUCATION

B.E. in Electronics & Telecommunication Engineering
SIES Graduate School of Technology, Mumbai

2021 – 2025
CGPA: 8.27 / 10.0

TECHNICAL SKILLS

Design: UI/UX Redesign, Wireframing, High-Fidelity Prototyping, Design Systems

Tools: Figma (Advanced Auto Layout, Components, Variables), Canva

Development: Flutter, Dart, Firebase, IoT Integration (ESP32), Python, SQL

Engineering: Sensor Data Visualization, Hardware-Software Logic, Digital Logic (Verilog)

SELECTED PROJECTS

ShipSense – Logistics Management Platform (Final Year Project)

2024 – 2025

Figma • Flutter • Esp 32 • Firebase

- Redesigned the user interface of my final-year IoT shipment tracking project to improve usability, visual hierarchy, and accessibility for logistics managers and drivers.
- Designed separate role-based experiences for managers and drivers, including dashboards, live shipment tracking, temperature and humidity monitoring, navigation, and profile management.
- Built a reusable design system using Auto Layout, components, typography, spacing, and interactive prototypes to ensure consistency across the application.
- Collaborated on the implementation of the solution using Flutter with ESP32, GPS, DHT22 sensors, Firebase, and GSM connectivity for real-time shipment monitoring.

Figma UI Practice – Agency Landing Page

June 2026

Visual Identity • Responsive Layouts • Figma

- Designed a landing page following an 8-point grid system.
- Created reusable components and Auto Layout variants.
- Practiced Auto Layout, grid systems, typography hierarchy, and responsive design principles.
- Practiced Improved proficiency in creating structured, scalable UI layouts.

Laser Security System using FPGA

Jan 2024 – Apr 2024

Verilog • Digital Logic • Hardware Design

- Implemented a smart hardware security system using **laser-LDR** and **RFID** for secure access.
- Built low-latency logic in **Verilog** to trigger instant GSM alerts, bridging the gap between physical hardware and digital notifications.

PUBLICATIONS

ShipSense: Live Shipment Monitoring and Alerting via Mobile-Integrated IoT System

April 2025

International Journal of Innovative Research in Computer and Communication Engineering (IJIRCCE)

Mentor: Prof. Vaishali Mangrulkar

Published research presenting the design and implementation of an IoT-based shipment monitoring system integrating ESP32, GPS, DHT22 sensors, GSM connectivity, and a Flutter mobile application for real-time cargo tracking and environmental monitoring.

ACHIEVEMENTS

- Runner-up, **TechXter 14.0** College-Level Paper Presentation Competition.
- **Training:** Flutter & Cloud Integration (**TechBairn**) | FPGA Design & Verilog Workshop.